IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): FUNABASHI Serial No.:

09/902.672

Filed:

July 12, 2001

For:

METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Group:

2825

Examiner:

L. Malsawma

PETITION FOR EXTENSION OF TIME

Assistant Commissioner for Patents

Washington, D.C. 20231

May 16, 2003

Sir:

In the matter of the above-identified application, applicant hereby respectfully

petitions for an extension of time to permit filing a response within the second month subsequent to the filing of a Notice of Appeal on January 16, 2003. A Credit Card Payment Form in the amount of \$410.00 to cover the required

fee for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135

> William I. Solomon Registration No. 28,565

(Case No. 843.37558VX2).

05/19/2003 MDAMTE1 00000077 09902672 02 FC:1252

Respectfully submitted,

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